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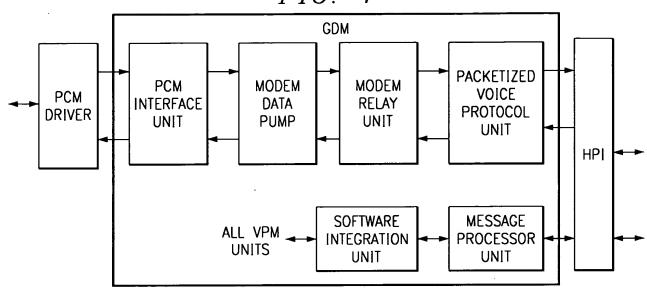


FIG. 8 MODEM RELAY UNIT (MRU) DD **MRP** MND EVENT/DATA QUÉUE **NETWORK INTERFACE NETWORK** MODEM RECEIVE RELAY STATE  $\mathsf{CM}$ QUEUE EVENT/DATA QUEUE MACHINE **INTERFACE NETWORK** MODEM JITTER BUFFER DRIVER **OVERFLOW** QUEUE i RTP MODULE i MODEM DATA PUMP API **MDPU** 

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FIG. 10

	RTP HEADER	(12 BYTES)
RTP PACKET	RTP	MRU HEADER (1-2 BYTES)
	PAYLOAD	MRU PAYLOAD

FIG. 11

IT 7	BIT 6	BIT 5	BIT 4	BIT 3	BIT 2	BIT 1	BIT 0		
١	/	P .	Χ		С	С			
M				PT					
	SI	EQUEN	ICE N	IUMBE	R (MS	S)			
	S	EQUE	NCE N	IUMBE	R (LS	5)			
		TIN	MESTA	MP (N	1S)				
		TIME	STAMI	P (MS	5–1)				25
	·	TIME	STAMI	P (MS	5–2)				
	,	TIN	/ESTA	MP (L	.S)				
SYNCHRONIZATION SOURCE (MS)									
SYNCHRONIZATION SOURCE (MS-1)									
SYNCHRONIZATION SOURCE (MS-2)									
SYNCHRONIZATION SOURCE (LS)									

MRU PROTOCOL VERSION

BIT 7	BIT 6	BIT 5	BIT 4	BIT 3	BIT 2	BIT 1	BIT 0
PRC	PROTOCOL STATE OR HANDSHAKE STATE						
	ADDI	TIONA	L INF	ORMA	TION	BYTE	

BIT 7	BIT 6	BIT 5	BIT 4	BIT 3	BIT 2	BIT 1	BIT. 0
		DATA	A PAC	KET 1	YPE		
	B	YTE 0	OF I	MODE	M DAT	Ā	
	В	YTE 1	OF I	MODE	M DAT	Ά	
			0				
	0						
	BY1	E N-	1 OF	MOD	EM D	ATA	

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<u>а</u>	PROTOCOL STATE	DESCRIPTION
L	OFFLINE	IDLE STATE
	CARRIER LOSS	SILENCE ON THE ANALOG LINE, CARRIER LOSS
<u> </u>	HANDSHAKE	MODEM DATA PUMPS WILL BEGIN HANDSHAKING PROCESS
<u> </u>	CONNECT	LOCAL HANDSHAKE SESSION IS PASSED AND READY FOR MODEM RELAY
	RELAY	MODEM RELAY TAKES PLACE
₩	MODEM HANDSHAKE	STATE PACKETS
Ĥ	HANDSHAKE STATE	DESCRIPTION
	V25	V.25 ANSWER TONE IS DETECTED ON THE LOCAL ANALOG LINE
	V25PR	V.25 ANSWER TONE WITH PHASE REVERSALS IS DETECTED ON THE LOCAL ANALOG LINE
	V21	V.21 B1 SIGNAL IS DETECTED ON THE LOCAL ANALOG LINE
	USB1	V.22 USB1 SIGNAL IS DETECTED ON THE LOCAL ANALOG LINE
	Sı	V.22bis S1 SIGNAL IS DETECTED ON THE LOCAL ANALOG LINE
	SB1_1200	V.22bis SB1 SIGNAL @ 1200 IS DETECTED ON THE LOCAL ANALOG LINE
	SB1_2400	V.22bis SB1 SIGNAL @ 2400 IS DETECTED ON THE LOCAL ANALOG LINE
₩	MODEM DATA PACKETS	S
- 275	DATA TYPE	DESCRIPTION
<u> </u>	V21	V.21 DATA @ 300 bps
<u> </u>	V22	V.22 DATA @ 1200 bps
<u> </u>	V22BIS	V.22bis DATA @ 2400bps

FIG. 12

